SPECIFICATIONS

All Specifications Typical At Nominal Line, Full Load, and 25℃ Unless Otherwise Noted

INPUT SPECIFICATIONS :

Input Voltage Range..... ±10%
Input Surge Voltage (100ms max.)..... .5V..... 9Vdc max.
12V.....18Vdc max.
24V.....30Vdc max.
Input Filter .....Capacitive

OUTPUT SPECIFICATIONS :

Voltage Accuracy.....±3.0% max.
Ripple and Noise, 20MHz BW.....100mV p-p max.
Temperature Coefficient.....±0.05%/C max.
Short Circuit Protection.....Momentary 1sec. max.
Line Regulation (Note1)..... ±1.2% max.
Load Regulation (Note2)..... ±10% max.

GENERAL SPECIFICATIONS :

Efficiency.....See Table
Isolation Voltage.....1000 VDC min.
Isolation Resistance .....10^9 Ohms min.
Isolation Capacitance..... 10pF Typ.
Switching Frequency .....5&12V Input .....90KHz typ.
24V Input .....80KHz typ.
Operating Ambient Temperature Range..... -40℃ to +85℃
De-rating, Above 85℃ ..... Linearly to Zero power at 100℃
Case temperature (Note4).....+100℃ max.
Cooling.....Natural Convection
Storage Temperature Range ..... -55℃ to +125℃
Humidity.....95% RH max. Non condensing
MTBF.....MIL-STD-217F,GB .....1.7Mhrs min
Dimensions:
5&12V input .....0.46 x 0.24 x 0.40inches
(11.6 x 6.0 x 10.2mm)
24V input .....0.46 x 0.30 x 0.40inches
(11.6 x 7.5 x 10.2mm)
Case Material.....Non-conductive black plastic
Weight.....5&12V Input 1.3g
24V Input 1.7g

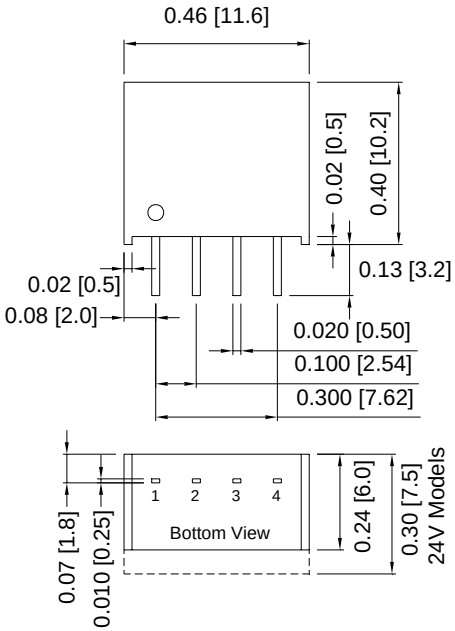
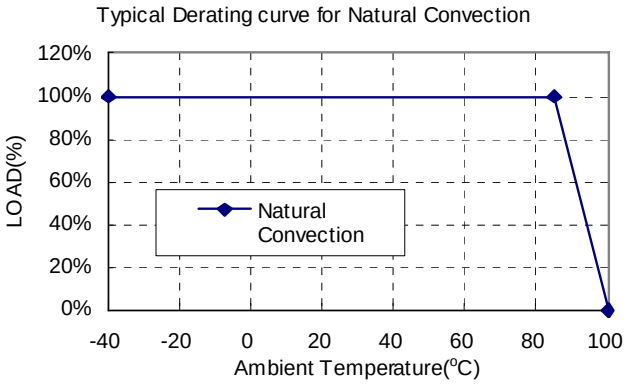
NOTE :

1. Line regulation is per 1.0% change in input voltage
2. Load regulation is for load change from 100% to 20%
3. The output noise is measured with 0.33uF ceramic capacitor.
4. Maximum case temperature under any operating condition should not be exceeded 100℃.
5. The EC1TA2XN input terminal need to parallel with 4.7uF ceramic capacitor

Dimensions:

All Dimensions In Inches (mm)

Tolerances	Inches	Millimeters
	±0.01	±0.25
Pin	±0.002	±0.05



PIN CONNECTION	
Pin	Single
1	-Vin
2	+Vin
3	-Vout
4	+Vout